



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:) Group Art Unit: 2812
Yukio TANAKA et al.) Examiner:
Serial No. 10/618,850) CERTIFICATE OF MAILING I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 2-6-2004
Filed: July 15, 2003	
For: ACTIVE MATRIX TYPE	
SEMICONDUCTOR DISPLAY	
DEVICE) adulu M Stamper

REQUEST FOR CORRECTED FILING RECEIPT

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The Filing Receipt received in the above-identified application has errors thereon due to Patent Office error as shown in red on the attached copy. A copy of the Application Data Sheet is also enclosed verifying the residence address of inventor Yukio Tanaka.

In view of the above, it is requested that a Corrected Filing Receipt be issued.

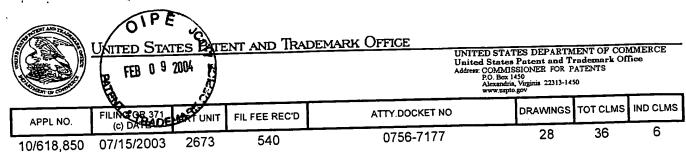
Respectfully submitted,

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31780 ERIC ROBINSON PMB 955 21010 SOUTHBANK ST. POTOMAC FALLS, VA 20165 **CONFIRMATION NO. 4342**

FILING RECEIPT

OCO0000011775234

434 12/en

Date Mailed: 01/28/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

Semiconductor Energy Laboratory Co, Ltd., Atsugi-shi, JAPAN;

Domestic Priority data as claimed by applicant

This application is a DIV of 10/295,886 11/18/2002 PAT 6,635,505 which is a DIV of 09/438,432 11/12/1999 PAT 6,489,952

Foreign Applications

JAPAN 10-326470 11/17/1998 JAPAN 11-086202 03/29/1999

If Required, Foreign Filing License Granted: 01/28/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Active matrix type semiconductor display

Preliminary Class

345

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